

Substitute for form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)				Complete if Known	
				Application Number	09/849822
				Filing Date	05/04/01
				First Named Inventor	G. Glenn Henry Thomas C. McDonald
				Group Art Unit	2183
				Examiner Name	David Hulsman
Sheet	1	of	2	Attorney Docket Number	CNTR.2050

U.S. PATENT DOCUMENTS						
Examiner Initials	Cite No. ¹	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code ²			
DTT	AA	5142634		Fite et al	08/25/1992	
DTT	AB	5163140		Stiles et al	11/10/1992	
DTT	AC	5353421		Emma et al	10/04/1994	
DTT	AD	5404467		Saba et al	04/04/1995	
DTT	AE	5530825		Black et al	06/25/1996	
DTT	AF	5867701		Brown et al	02/02/1999	
DTT	AG	5948100		Hsu et al	09/07/1999	
DTT	AH	5978909		Oded Lempel	11/02/1999	
DTT	AI	6044459		Bae et al	03/28/2000	
DTT	AJ	6601161		Rappoport et al	07/29/2003	
DTT	AK	6647467		Eric M. Dowling	11/11/2003	

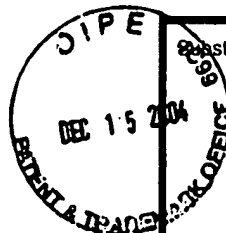
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⁴For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁶Applicant is to place a check mark here if English language Translation is attached.

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OTHER PRIOR ART-NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
DJH	BA	SAKAMOTO ET AL; <u>Microarchitecture Support for Reducing Branch Penalty in a Superscaler Processor</u> ; pp. 208-216; IEEE 1996; Mitsubishi Electric Corp., System LSI Laboratory, 4-1 Mizuhara, Itami, Hyogo 664, Japan.	
DJH	BB	YEH ET AL; <u>ALTERNATIVE IMPLEMENTATION OF TWO-LEVEL ADAPTIVE BRANCH PREDICTION</u> ; 19 th Annual International Symposium on Computer Architecture, pp. 124-134, May 19-21, 1992, Gold Coast, Australia.	
DJH	BC	CHANG ET AL; <u>ALTERNATIVE IMPLEMENTATIONS OF HYBRID BRANCH PREDICTORS</u> ; Proceedings of MICRO-28, 1995, IEEE.	
DJH	BD	MC FARLING, SCOTT; <u>WRL TECHNICAL NOTE TN-36, "Combining Branch Predictors</u> , June 1993, Western Research Laboratory, 250 University Ave., Palo Alto, CA 94301	
DJH	BE	IEEE 100; <u>THE AUTHORITATIVE DICTIONARY OF IEEE STANDARDS TERMS</u> ; Seventh Edition, IEEE, Standards Information Network, IEEE Press,	
DJH	BF	BRAY ET AL; <u>STRATEGIES FOR BRANCH TARGET BUFFERS</u> ; Technical Report No. CSL-TR-91-480, June 1991.	

Examiner Signature	<i>David J. Huisman</i>	Date Considered	3-17-05
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